

Electrofluorescent plate

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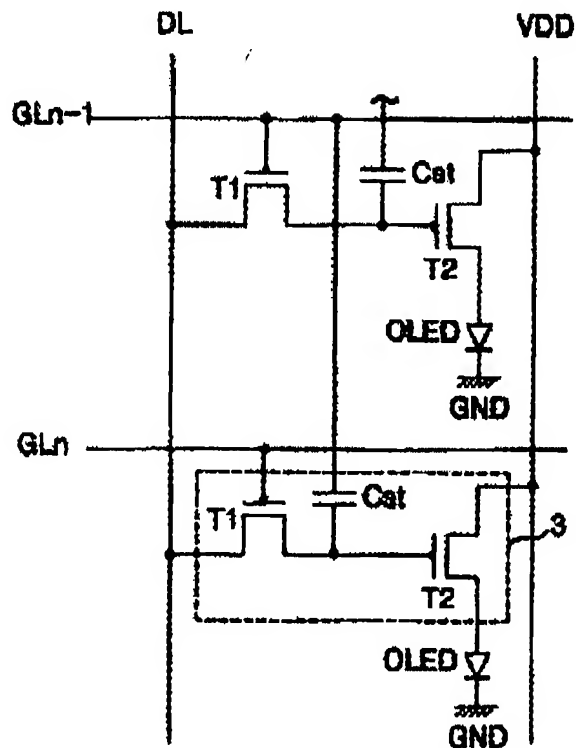
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An electro-luminescence panel that is adaptive for maximizing a capacitance of a storage capacitor. A plurality of electro-luminescence cells are arranged at crossings between gate lines and data lines in the panel. An electro-luminescence cell driving circuit drives the electro-luminescence cells. In the driving circuit, a power supply supplies power to the electro-luminescence cells. A first thin film transistor is connected between the power supply and the electro-luminescence cell. A second thin film transistor is connected between the data line and a gate electrode of the first thin film transistor to serve a switch of the electro-luminescence cell. A storage capacitor is connected between the gate electrode of the first thin film transistor and a pre-



stage gate line. Accordingly, a capacitance value of the storage capacitor is maximized with the aid of the pre-stage gate line upon formation of the storage capacitor, thereby preventing flicker caused by a kickback phenomenon.

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